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APPLICANTS
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** CONTINUING DATA *****
 This application is a CON of PCT/JP02/08742 08/29/2002 ✓

** FOREIGN APPLICATIONS *****
 JAPAN 2001-262295 08/30/2001 ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 1	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 4
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TITLE
 Method of purging semiconductor manufacturing apparatus and method of manufacturing semiconductor device

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